

<b>Notice of References Cited</b>	Application/Control No. 09/930,976	Applicant(s)/Patent Under Reexamination KAO ET AL.	
	Examiner Latha Ravindran	Art Unit 2183	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-6,049,862	04-2000	Bauer et al.	712/208
	C	US-			
	D	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

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**NON-PATENT DOCUMENTS**

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	U	IBM, Opcode Remap and Compression in Hard Wired RISC Microprocessor, March 1990, IBM Technical Disclosure Bulletin, Vol. 32, Issue 10A, Page 349
	V	Araujo, Guido et al., Code Compression Based on Operand Factorization, 1998, Microarchitecture, 1998. Micro-3. Proceedings. 31st Annual ACM/IEEE International Symposium on, 30 Nov. -2 Dec. 1998, Pages 194 - 201
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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